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10005062

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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10005062	FILING DATE 12/03/2001	CLASS 715 327	SUBCLASS 16	GAU 2846	EXAMINER Carlowski
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**APPLICANTS: Watkins Daniel;

2825

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB DO NOT PUBLISH ☒

RESCIND ☐

Foreign priority claimed ☐ yes ☐ no
35 USC 119 conditions met ☐ yes ☐ no
Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

LS1-01-651

TITLE : Method and system for implementing incremental change to circuit design

U.S. DEPT. OF COMM./PAT. & TM-PTO-4361 (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ASSIGNED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drawn	Figs. Drawn
		Print Figs.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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